

INFORMAL

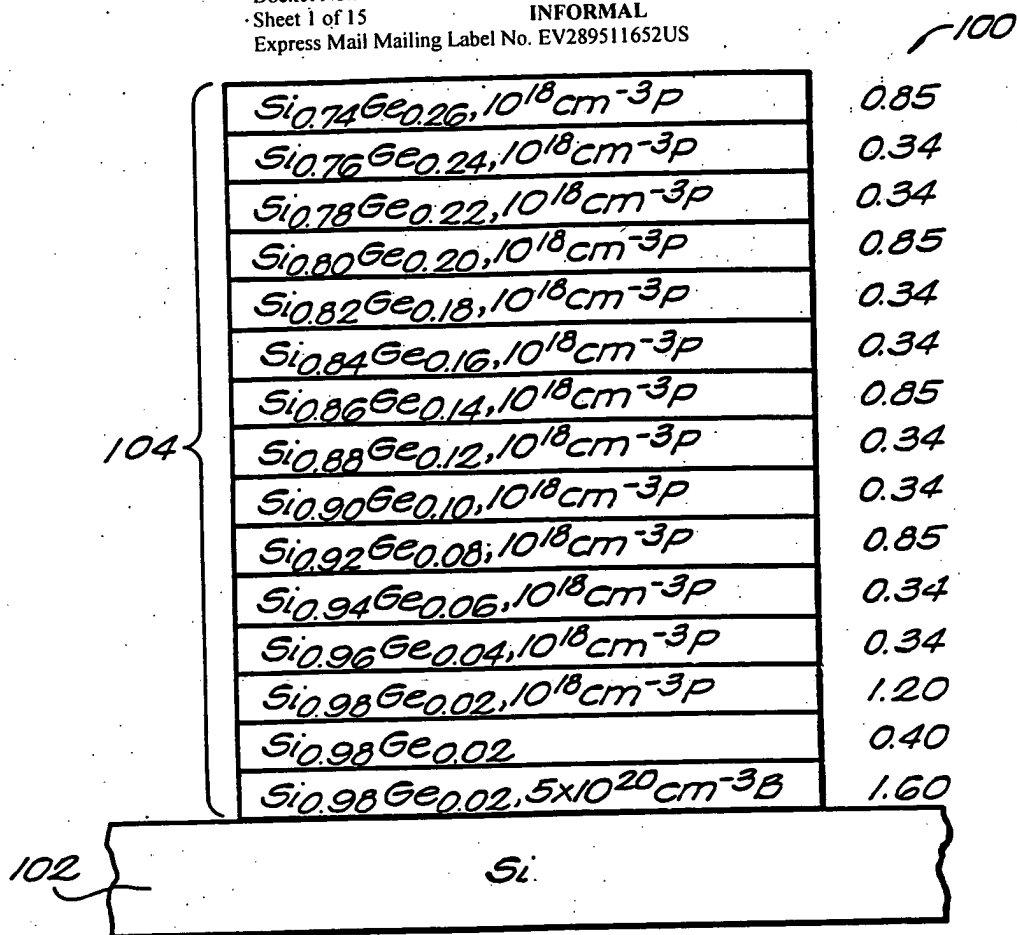


FIG. 1A

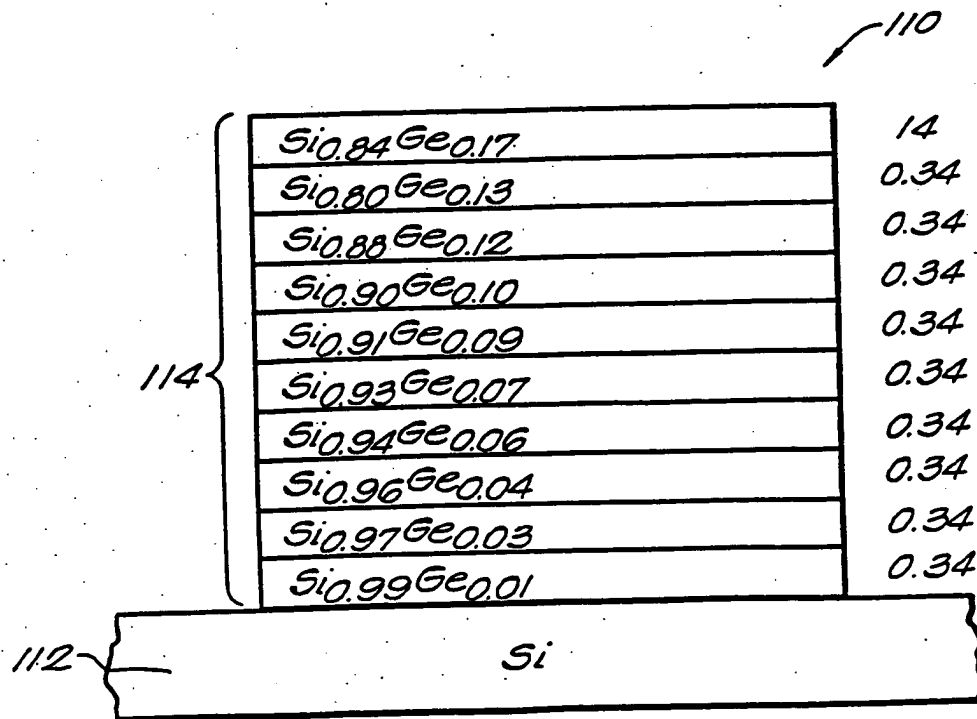
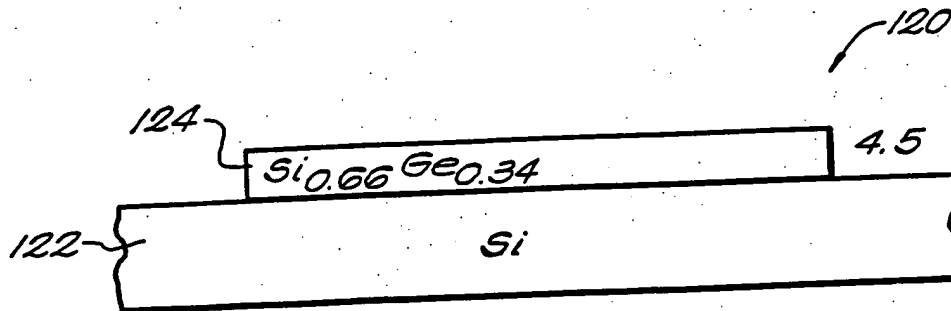
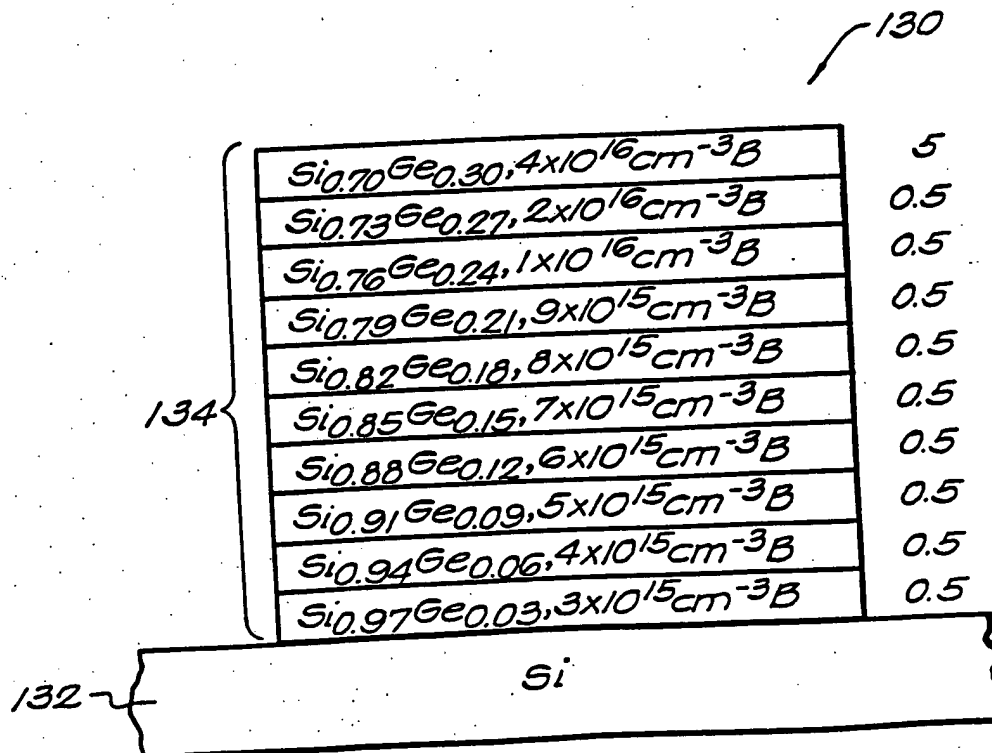


FIG. 1B

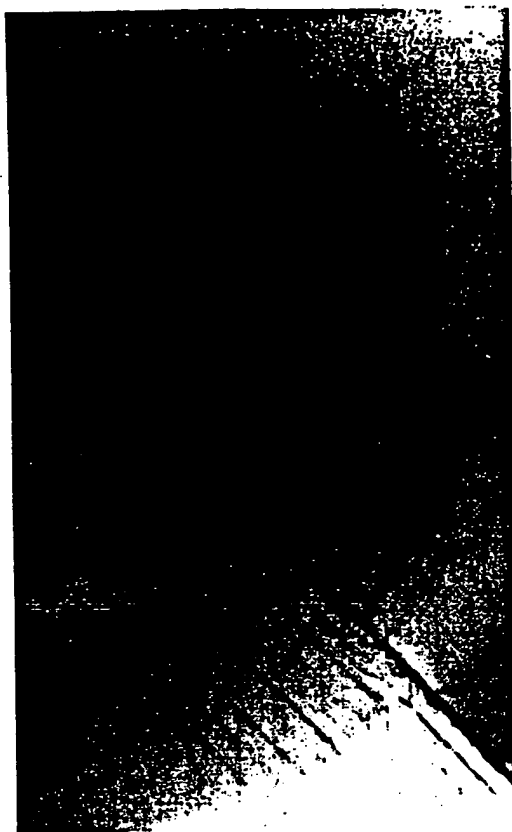


**FIG. 1C**



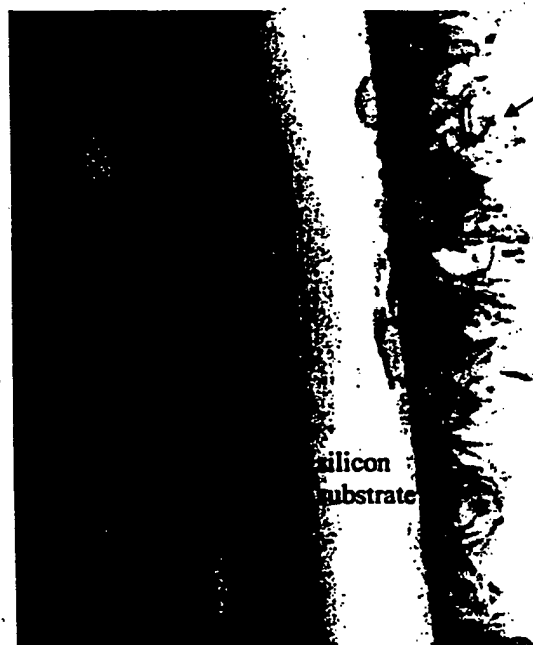
**FIG. 1D**

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*FIG. 2*

— 1.5  $\mu$ m



silicon  
substrate

threading  
dislocations

— 1.3  $\mu$ m

*FIG. 3*

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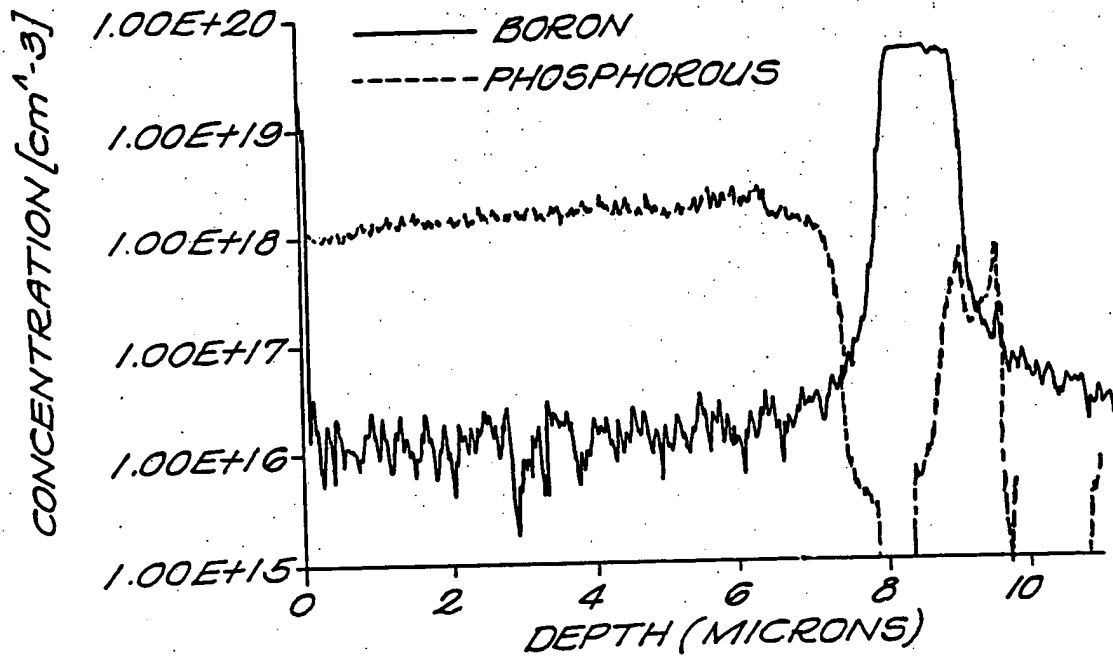


FIG. 4

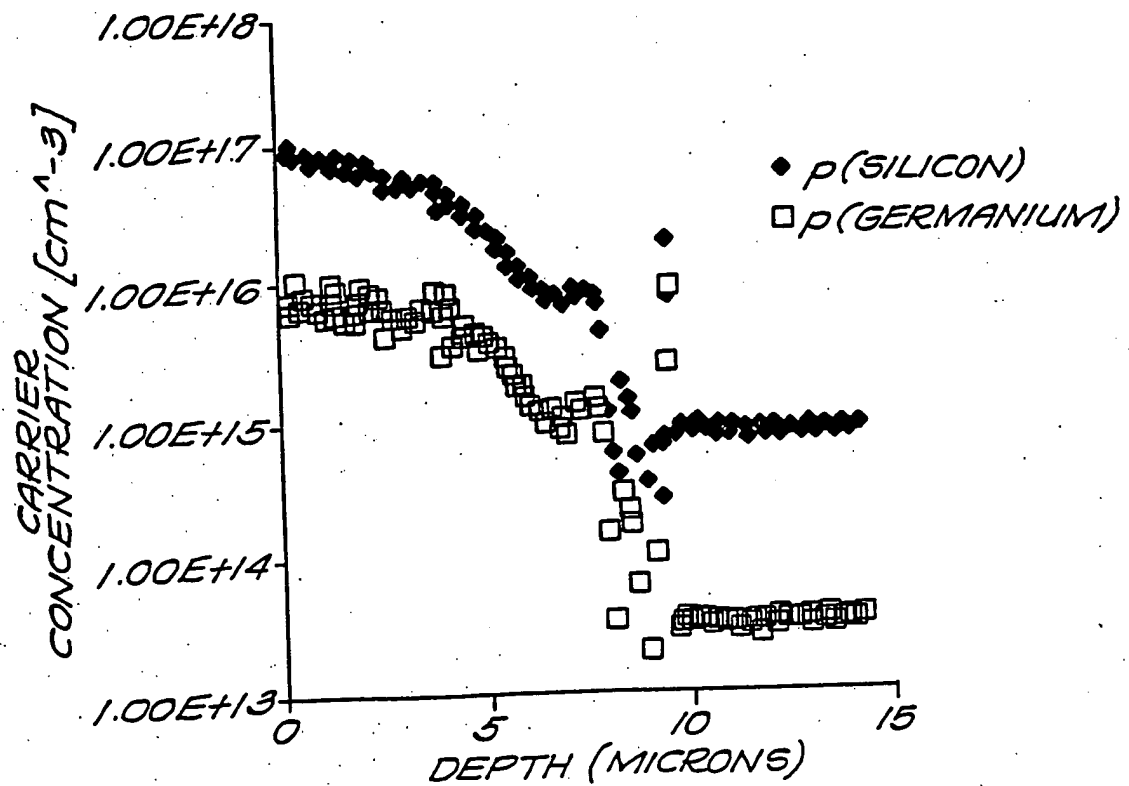
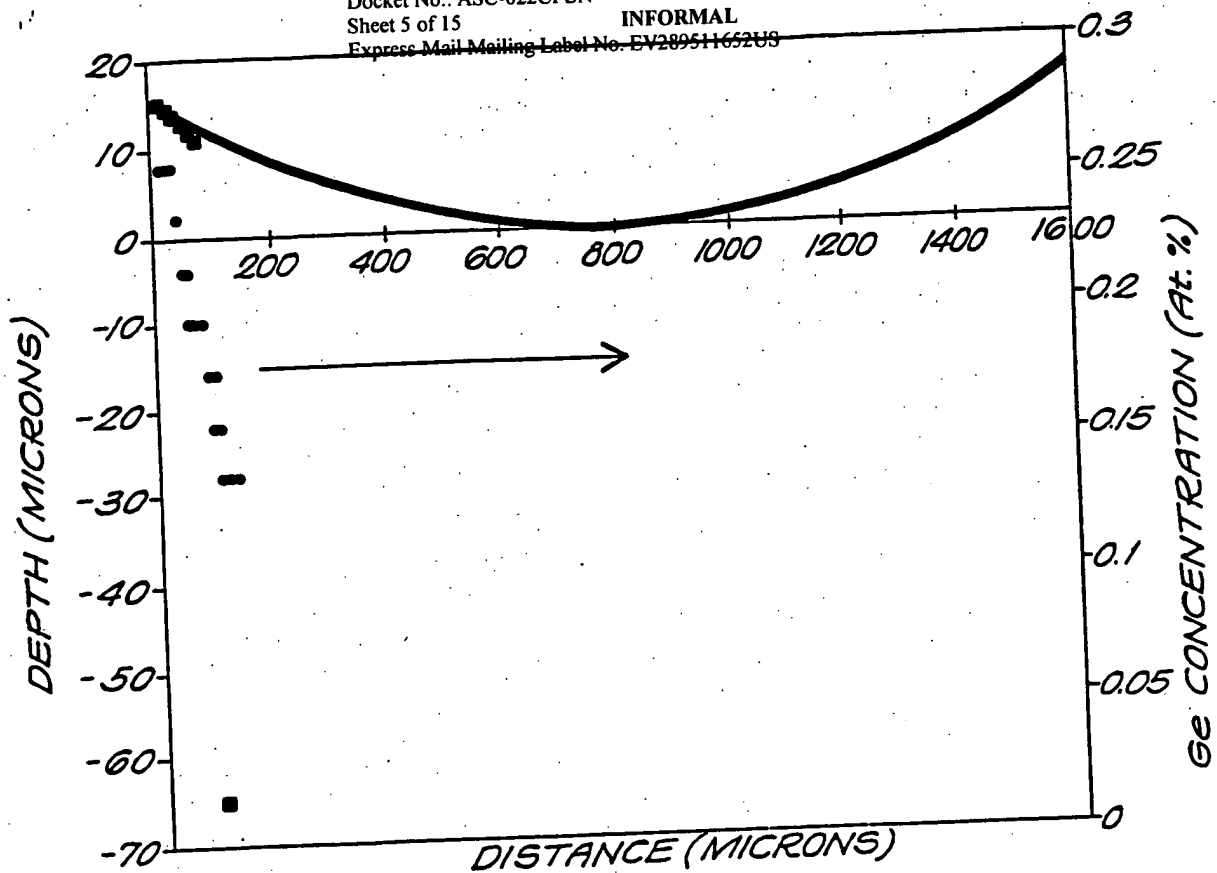
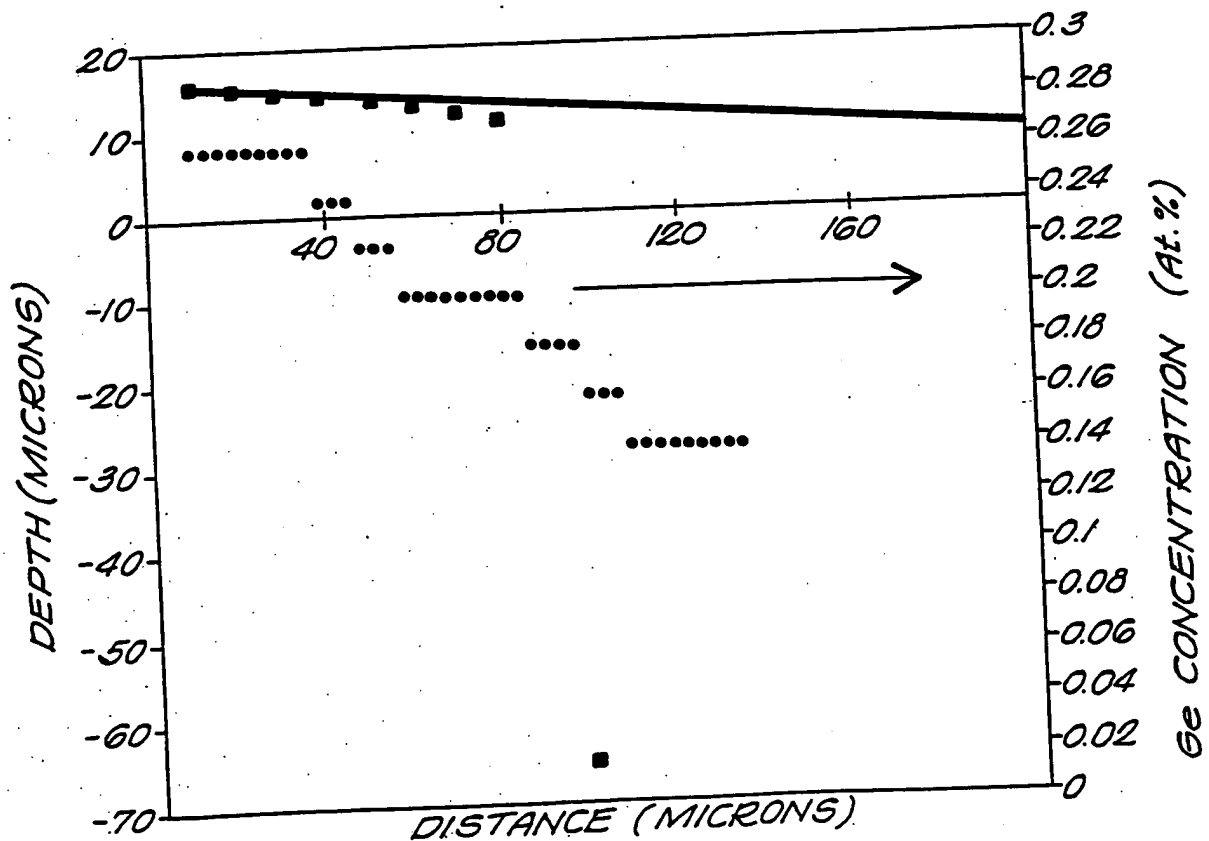


FIG. 5

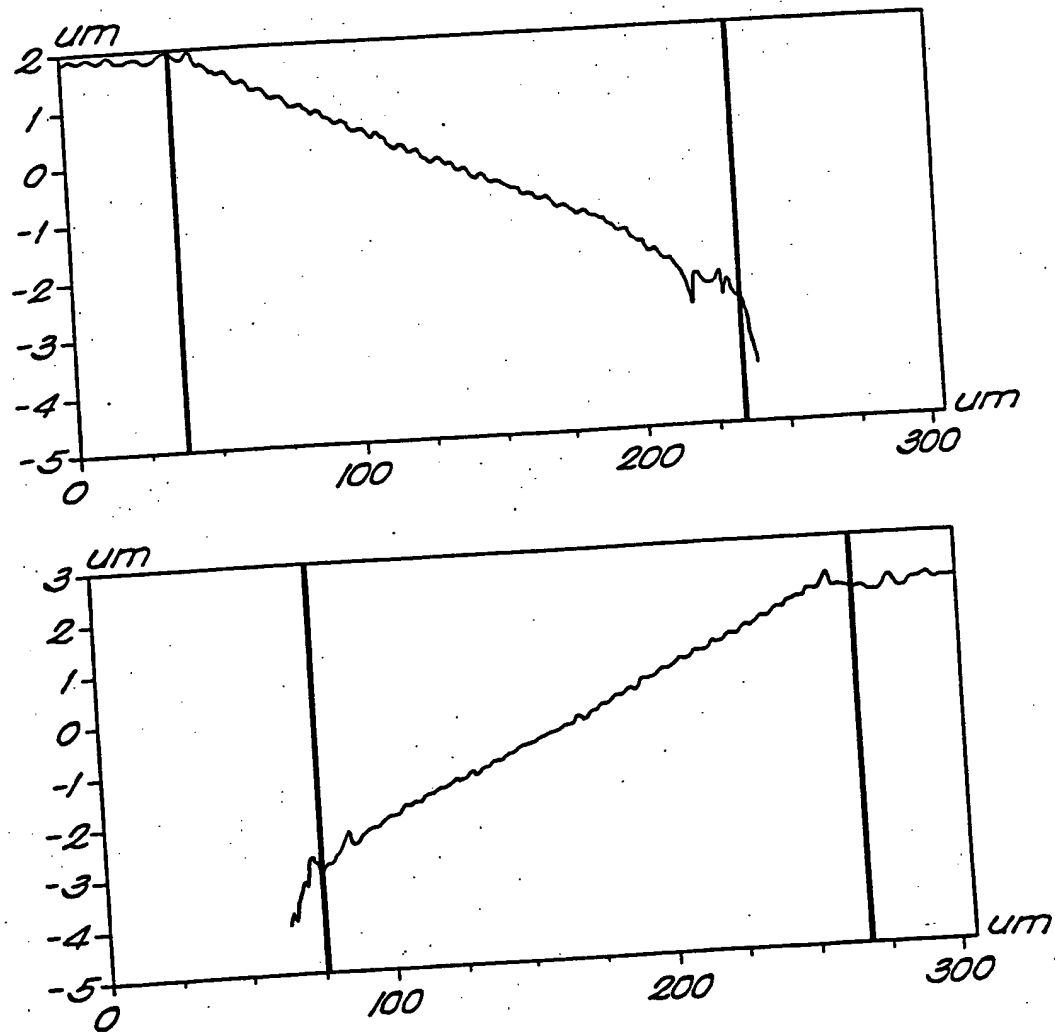
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**FIG. 6A**



**FIG. 6B**



**FIG. 7**

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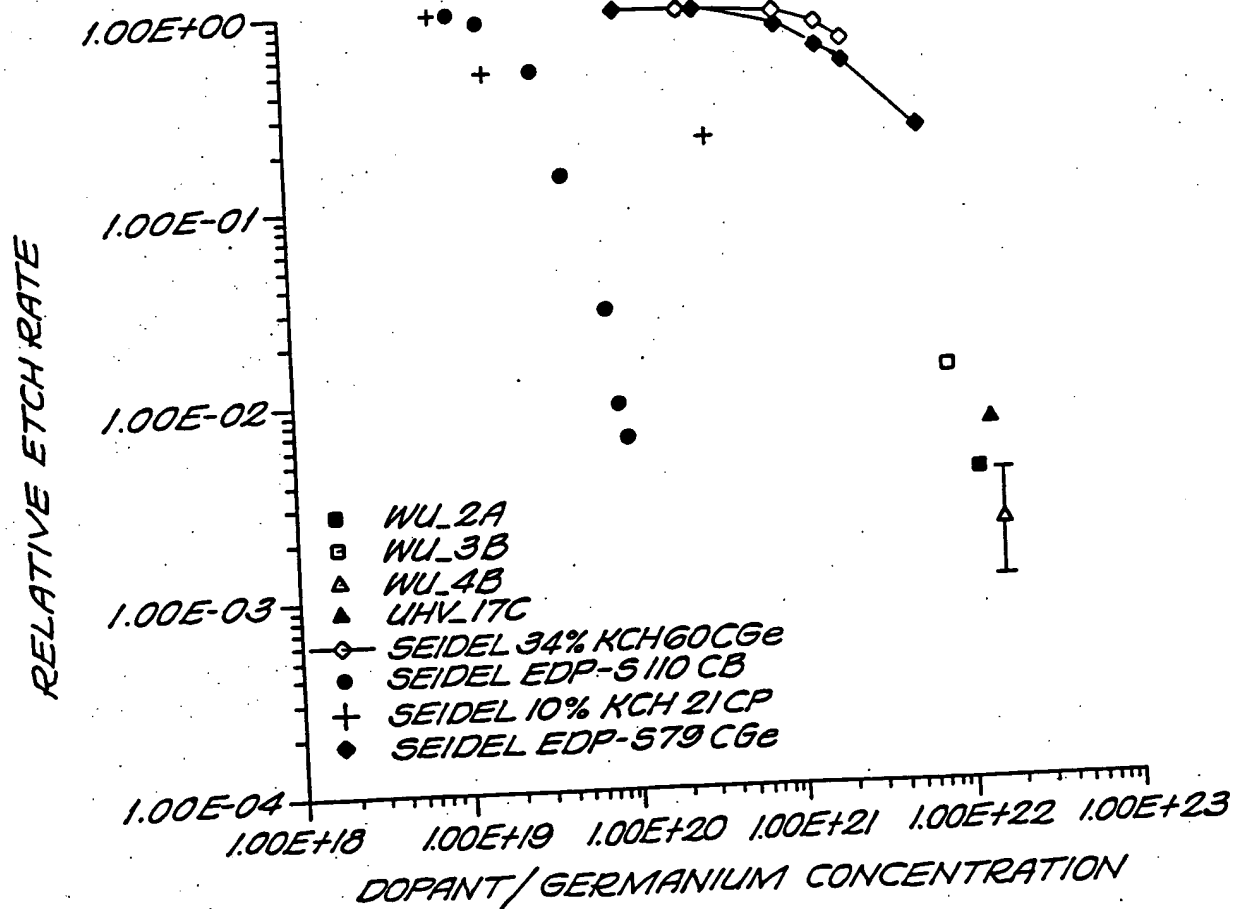
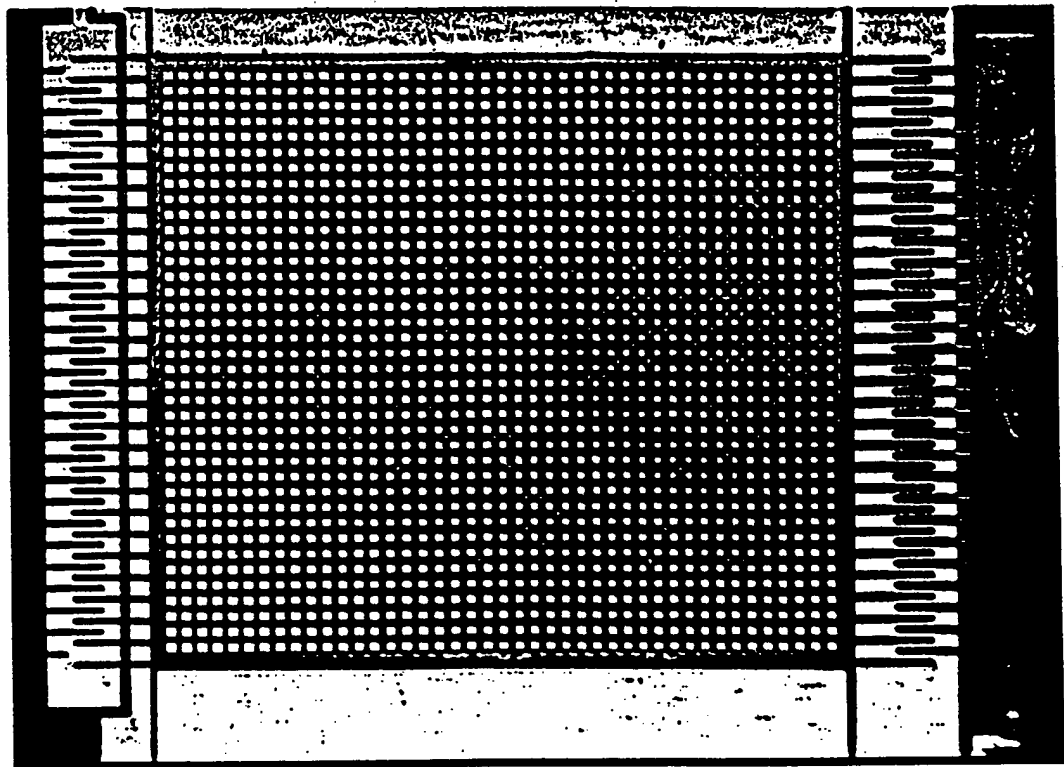


FIG. 8

900



*FIG. 9*



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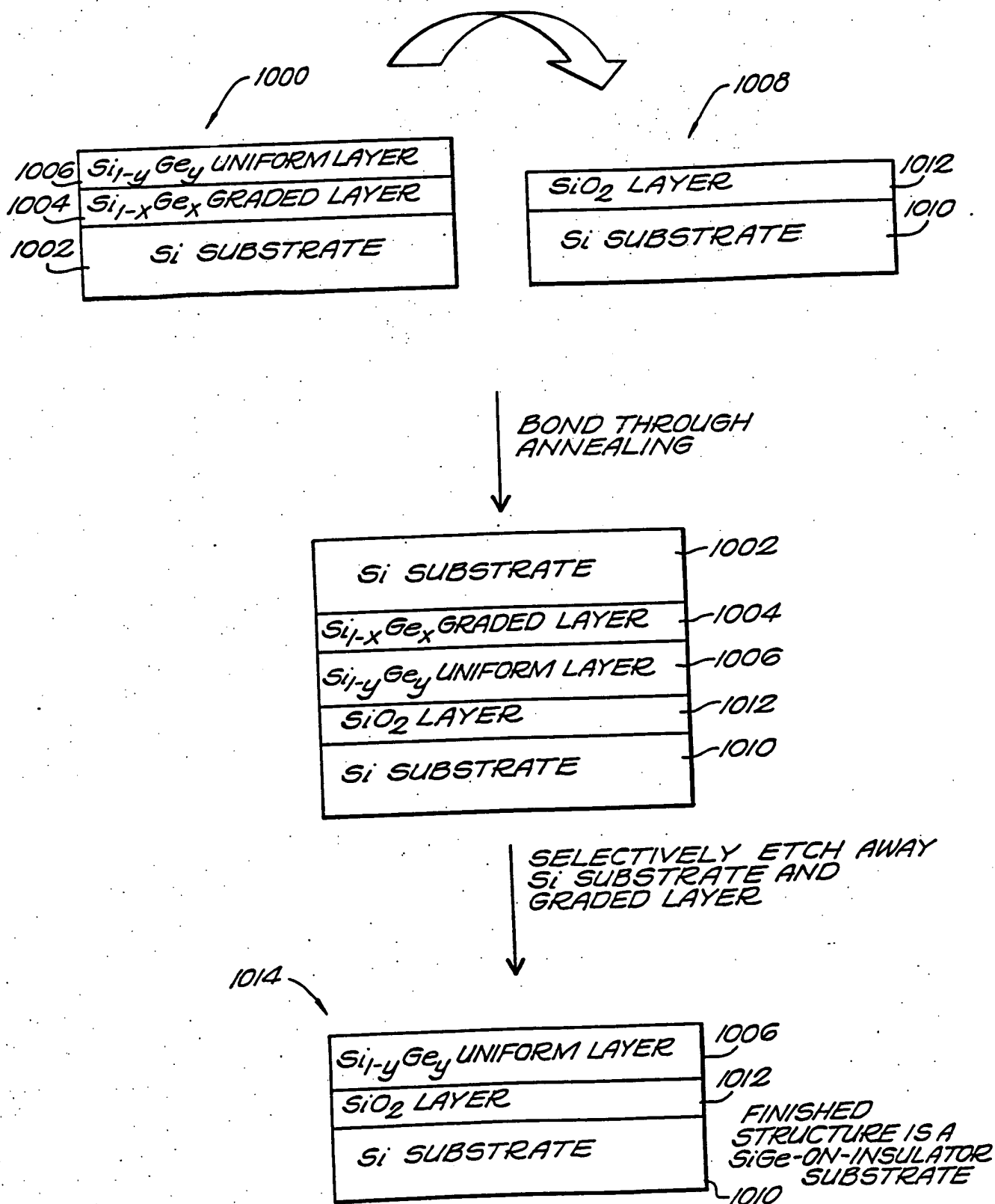


FIG. 10

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FIG. 11A

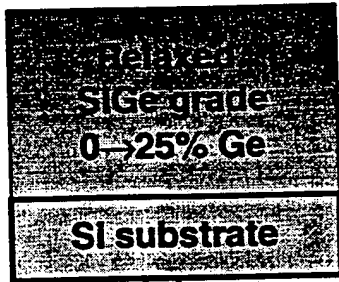


FIG. 11B

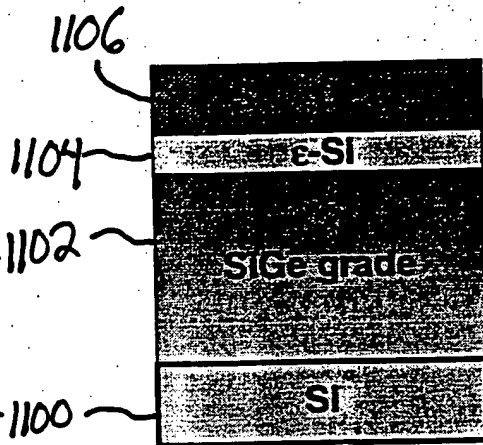


FIG. 11C

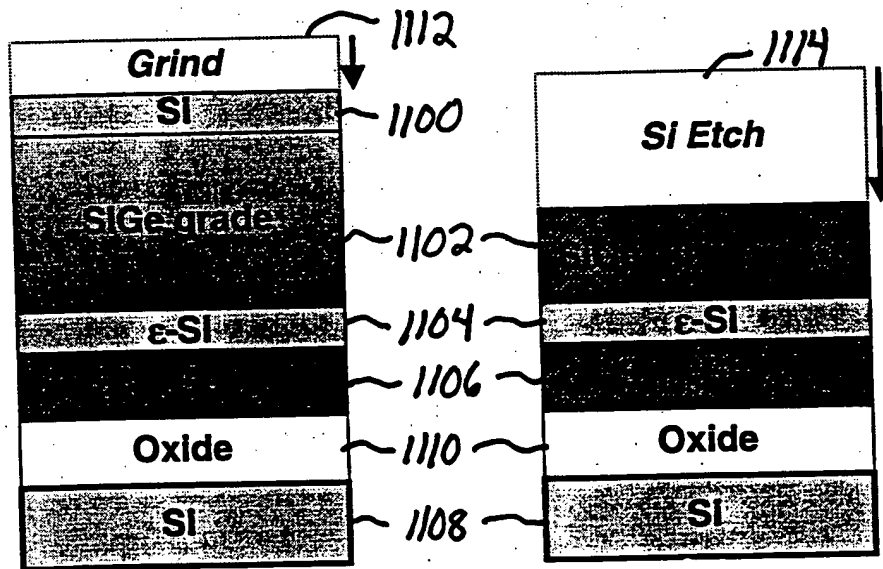
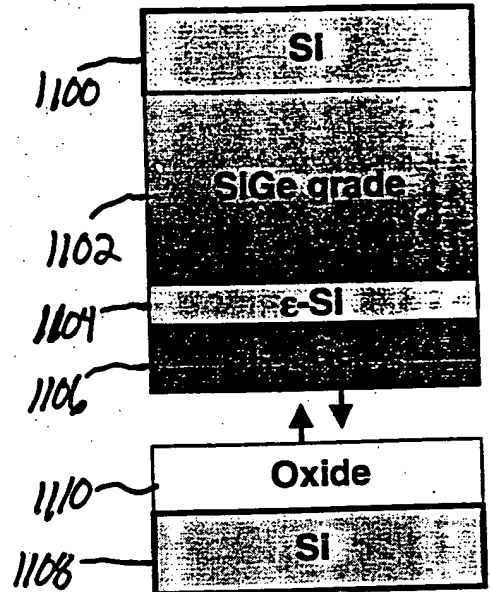


FIG. 11D

FIG. 11E

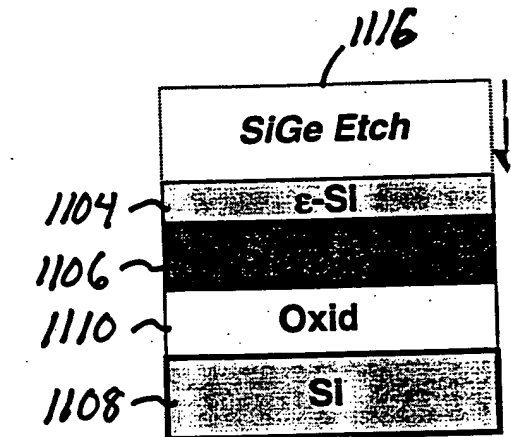


FIG. 11F

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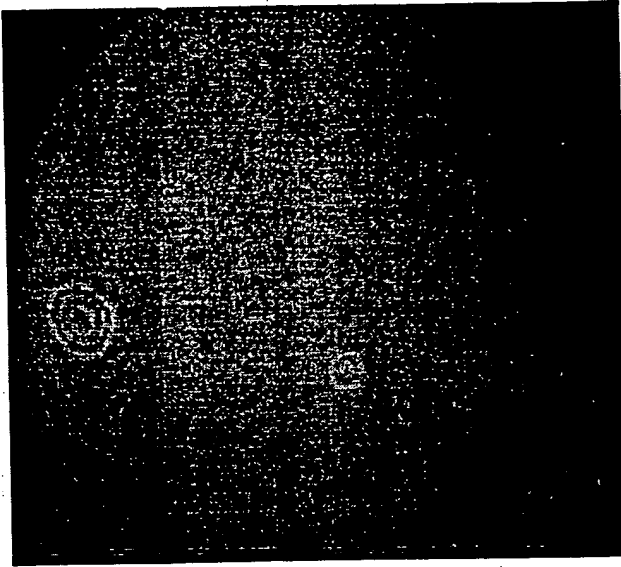


FIG. 12A

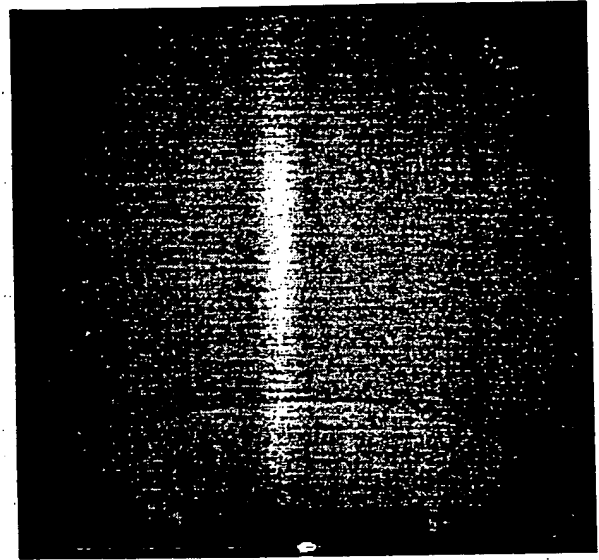


FIG. 12B

INFORMAL

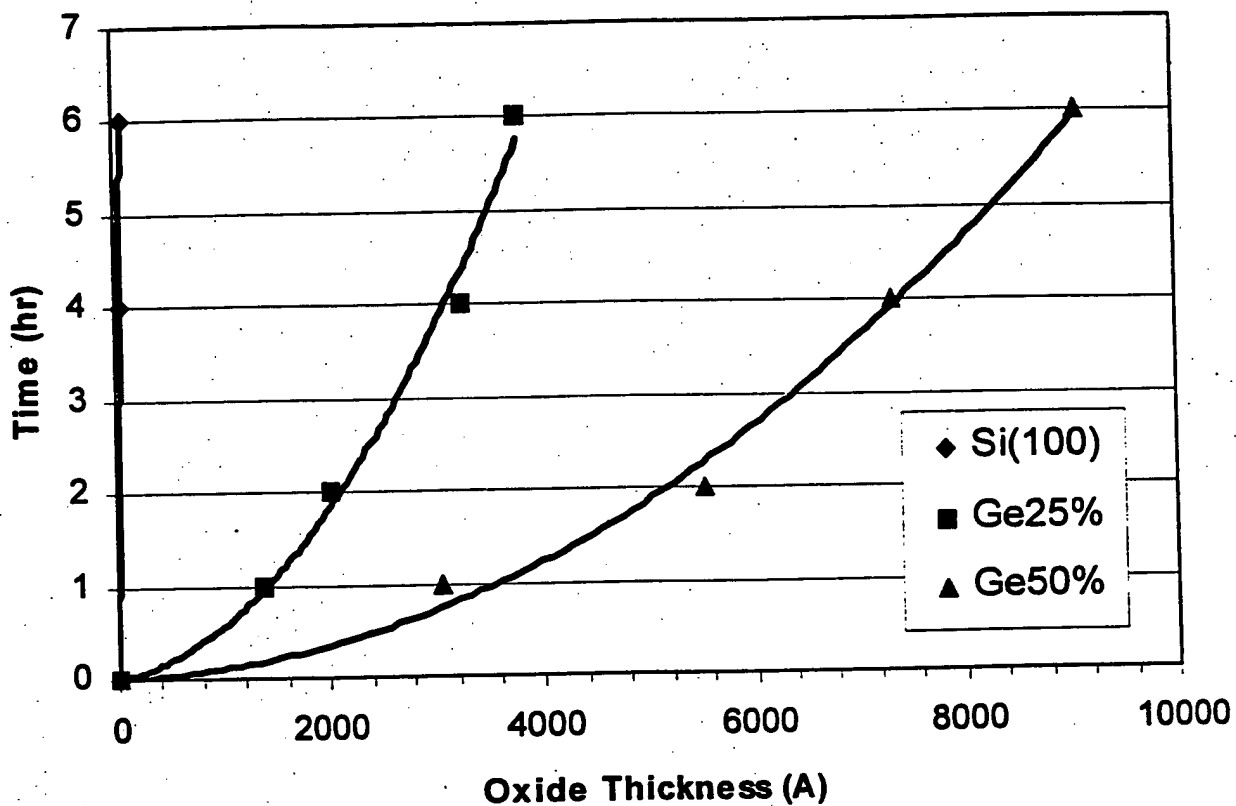


FIG. 13

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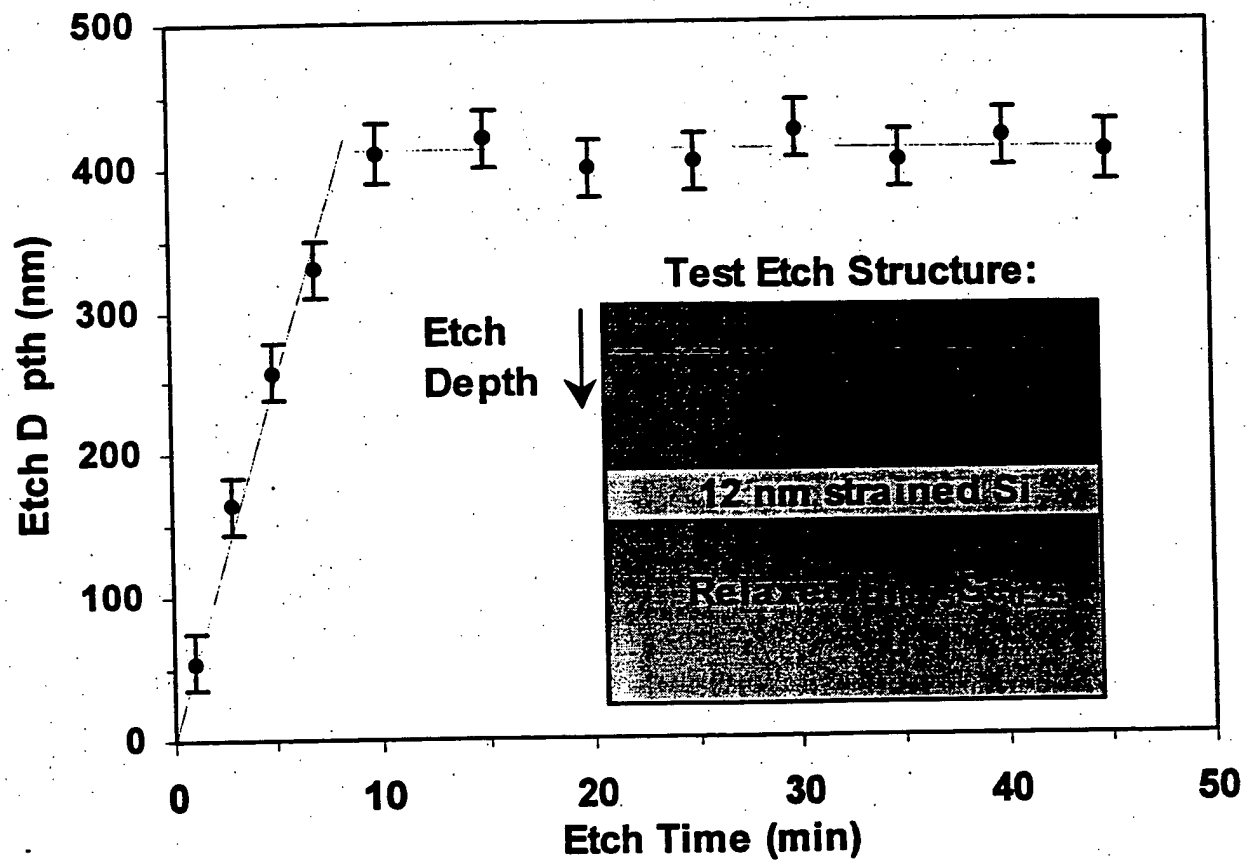


FIG. 14

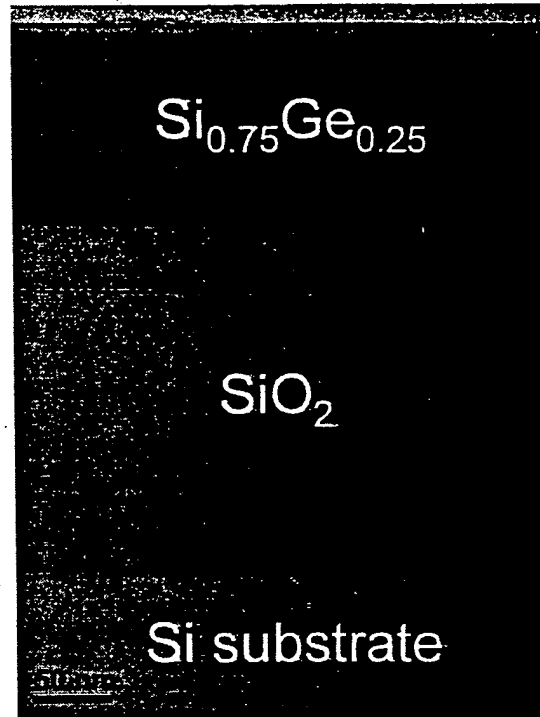


FIG. 15

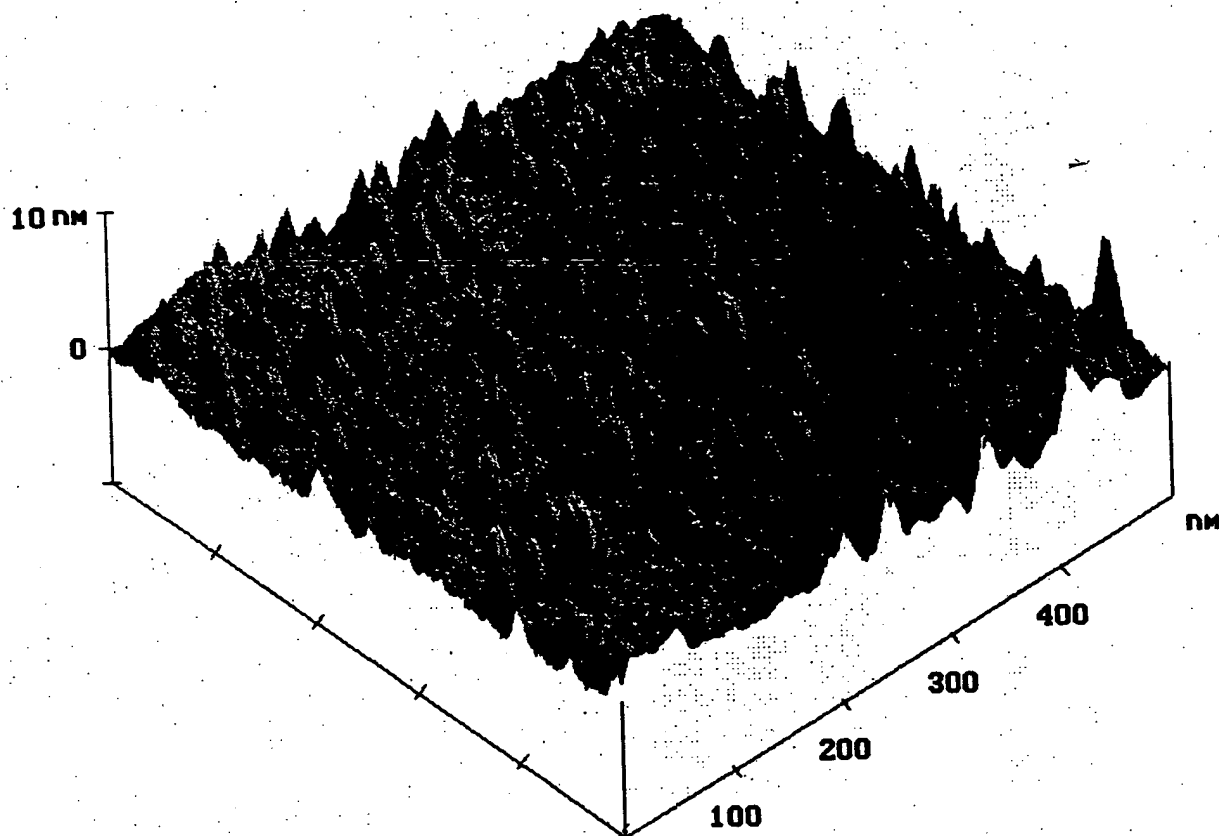


FIG. 16